

AMENDMENTS TO THE SPECIFICATION

In the Abstract

Please replace the abstract currently on record with the rewritten abstract attached hereto.

In the Specification

Please amend paragraph [0002] on page 1 of the specification as follows:

-- Resamplers, that is sampling-rate converters are used to convert a sampled digital input signal having one input sampling rate into a sampled digital output signal with a different output sampling rate. With a process of this invention there is an input signal having a sampling rate, or frequency, which is larger by an arbitrary factor (not necessarily by a whole number, or integer) than a symbol frequency or a chip frequency. With a ~~WCDMA-signal~~ Wideband Code Division Multiple Access (WCDMA) signal, every data symbol in a chip sequence is coded so that each symbol is of a plurality of chips. A binary change between two amplitude values can take place between the chips. The invention, however, is also suitable for other digital signals with then the term "chip frequency" being replaced by the term "symbol

frequency" or "symbol rate". Upon translating the input sampling rate into the symbol or chip frequency, the problem arises that the relationship between the input sampling rate and the symbol or chip frequency is only approximately known, since a timing generator of the resampler is not identical with a timing generator of the input sampling rate, and thus a drift between the two oscillators is possible. Further, an absolute phase position of the input sampling rate is unknown.